

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Dudeck et al.
Case: 3-6-11-34-7
Serial No.: Unassigned
Filing Date: Concurrently Herewith
Group: Unassigned
Examiner: Unassigned

Title: Method and Apparatus for Reducing Leakage Current
in a Read Only Memory Device Using Transistor Bias

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449. A copy of the listed item is enclosed.

1. U.S. Patent No. 6,459,615
2. U.S. Patent No. 6,512,700

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,



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Date: January 23, 2004

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

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U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
_____	6,459,615	10/01/02	McPartland et al.	365/185.1	
_____	6,512,700	01/28/03	McPartland et al.	365/185.28	

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.